

SSCE5V082P1

Ultra-low Capacitance Bidirectional Micro Packaged TVS Diodes for ESD Protection

Description

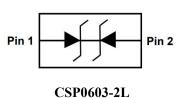
SSCE5V082P1 The is designed with SSC Punch-Through process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium. Also because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed, USB 3.0 super speed, VGA, DVI, HDMI, SDI and other high speed line applications.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), and EFT (electrical fast transients).

Feature

- ♦ 50W peak pulse power (tP = 8/20us)
- ♦ CSP0603-2L Package
- ♦ Working voltage: 5V
- Low clamping voltage
- Low capacitance(<0.25pF) for high-speed interfaces
- ♦ No insertion loss to 10.0GHz
- ♦ RoHS compliant
- ♦ Complies with following standards:
 - -IEC61000-4-2(ESD) ±20kV(contact), ±20kV(air)
 - -IEC61000-4-4 (EFT) 40A (5/50ns)
 - -IEC61000-4-5 (Lightning) 9A (8/20us)

PIN configuration





Marking

Applications

- High Speed Line: USB1.0/2.0/3.0/4.0, VGA, DVI, SDI
- High Definition Multi-Media Interface (HDMI1.3/1.4/2.0)
- ♦ Serial and Parallel Ports
- ♦ Notebooks, Desktops, Servers
- ♦ Projection TV
- ♦ Cellular handsets and accessories
- ♦ Portable instrumentation
- ♦ Peripherals

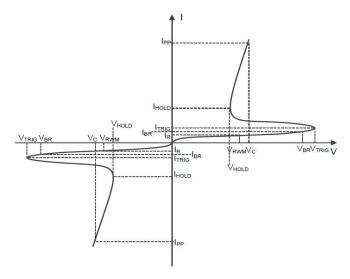
Mechanical data

- ♦ Lead finish:100% matte Sn (Tin)
- ♦ Mounting position: Any
- ♦ Qualified max reflow temperature:260 °C
- ♦ Device meets MSL 1 requirements
- ♦ Pure tin plating: 7 ~ 17 um
- ♦ Pin flatness: ≤3mil



• Electronic Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I⊤
lτ	Test Current
I _{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
P _{PP}	Peak Pulse Power
V_{TRIG}	Reverse Trigger Voltage
V_{TRIG}	Reverse Trigger Current
V _{HOLD}	Reverse Holding Voltage
I _{HOLD}	Reverse Holding Current
CJ	Junction Capacitance



Absolute maximum rating @Ta=25℃

Parameter		Symbol	Value	Unit	
Peak Pulse Power (8/20us)		P_PP	50	W	
Peak Pulse Current (8/20us)		I _{PP}	9	Α	
ESD Rating per IEC61000-4-2:	Contact	V	20	kV	
	Air	V_{ESD}	20		
Storage Temperature		T _{STG}	-55/+150	$^{\circ}$	
Operating Temperature		TJ	-55/+150	$^{\circ}$	
Lead Solder Temperature - Maximum (10 Se	econd Duration)	TL	260	${\mathbb C}$	

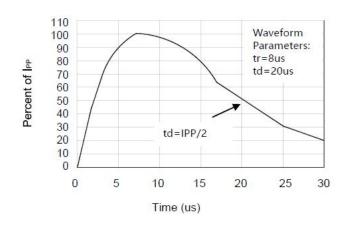
• Electrical Characteristics @Ta=25 $^{\circ}$ C

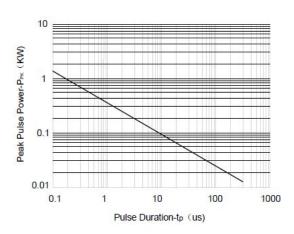
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Peak Reverse Working Voltage	V_{RWM}			5	5.5	V
Breakdown Voltage	V_{BR}	I _T = 1mA	6	9.5		V
Reverse Leakage Current	I _R	V _{RWM} =5.5V			1	μA
Clamping Voltage	Vc	$I_{PP} = 1A, t_P = 8/20us$		3.2		V
Clamping Voltage	Vc	I_{PP} =9A, t_P = 8/20us		5.5	8	V
Clamping Voltage	V _{C2}	I_{PP} =16A, t_P = 100ns		6.5		V
Dynamic resistance	R _{DYN}			0.23		Ω
Junction Congoitance	<u> </u>	V _R =1.0V, f = 1MHz		0.14	0.18	pF
Junction Capacitance	CJ	V _R =1.0V, f = 1GHz		0.13		pF

2/5



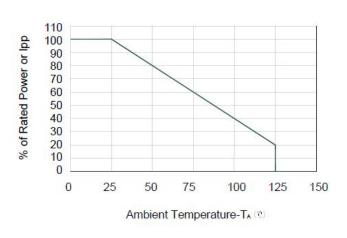
• Typical Performance Characteristics

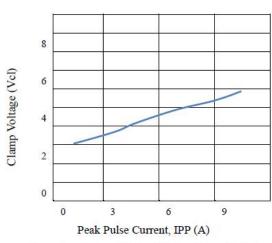




Pulse Waveform

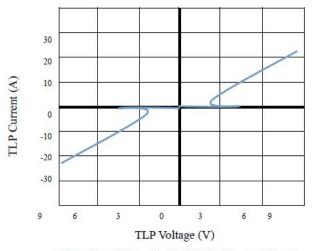
Non-Repetitive Peak Pulse Power vs. Pulse Time





Power Derating Curve

Clamping Voltage Vs Peak PulseCurrent(Ipp)



Clamping Voltage Vs Peak PulseCurrent(ITLP)



• Package Information

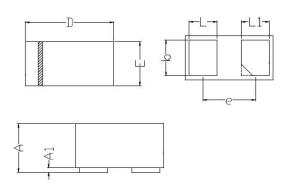
Ordering Information

Device	Package	Qty per Reel	Reel Size		
SSCE5V082P1	CSP0603-2L	9000	7 Inch		

Mechanical Data

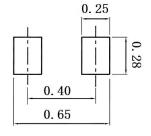
Case: CSP0603-2L

Case Material: Molded Plastic. UL Flammability

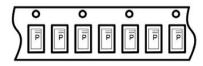


DIM	Millimeters				
MIM	Min	Max			
Α	0.230	0.330			
A 1	0.000 0.050				
А3	0.102REF				
D	0.550	0.650			
E	0.250	0.350			
b	0.220	0.270			
L	0.120	0.170			
L1	0.120	0.170			
е	0.40BSC				

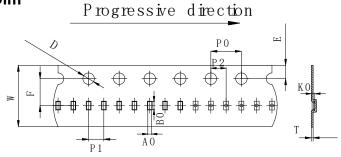
Recommended Pad outline



Device Orientation in Tape



CSP0603 Reel Dim



	PACKAGE	W	E	F	P0	D	P2	P1	Т	A0	В0	K0
CSP0603	8mm	1.75mm	3.5mm	4mm	1.5mm	2mm	2mm	0.23mm	0.34mm	0.67mm	0.4mm	
	±0.1	±0.1	±0.05	±0.1	±0.1	±0.05	±0.1	±0.02	±0.05	±0.05	±0.05	



DISCLAIMER

SSCSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. SSCSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICIENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.

OUR PRODUCT SPECIFICATIONS ARE ONLY VALID IF OBTAINED THROUGH THE COMPANY'S OFFICIAL WEBSITE, CRM SYSTEM, OR OUR SALES PERSONNEL CHANNELS. IF CHANGES OR SPECIAL VERSIONS ARE INVOLVED, THEY MUST BE STAMPED WITH A QUALITY SEAL AND MARKED WITH A SPECIAL VERSION NUMBER TO BE VALID.